

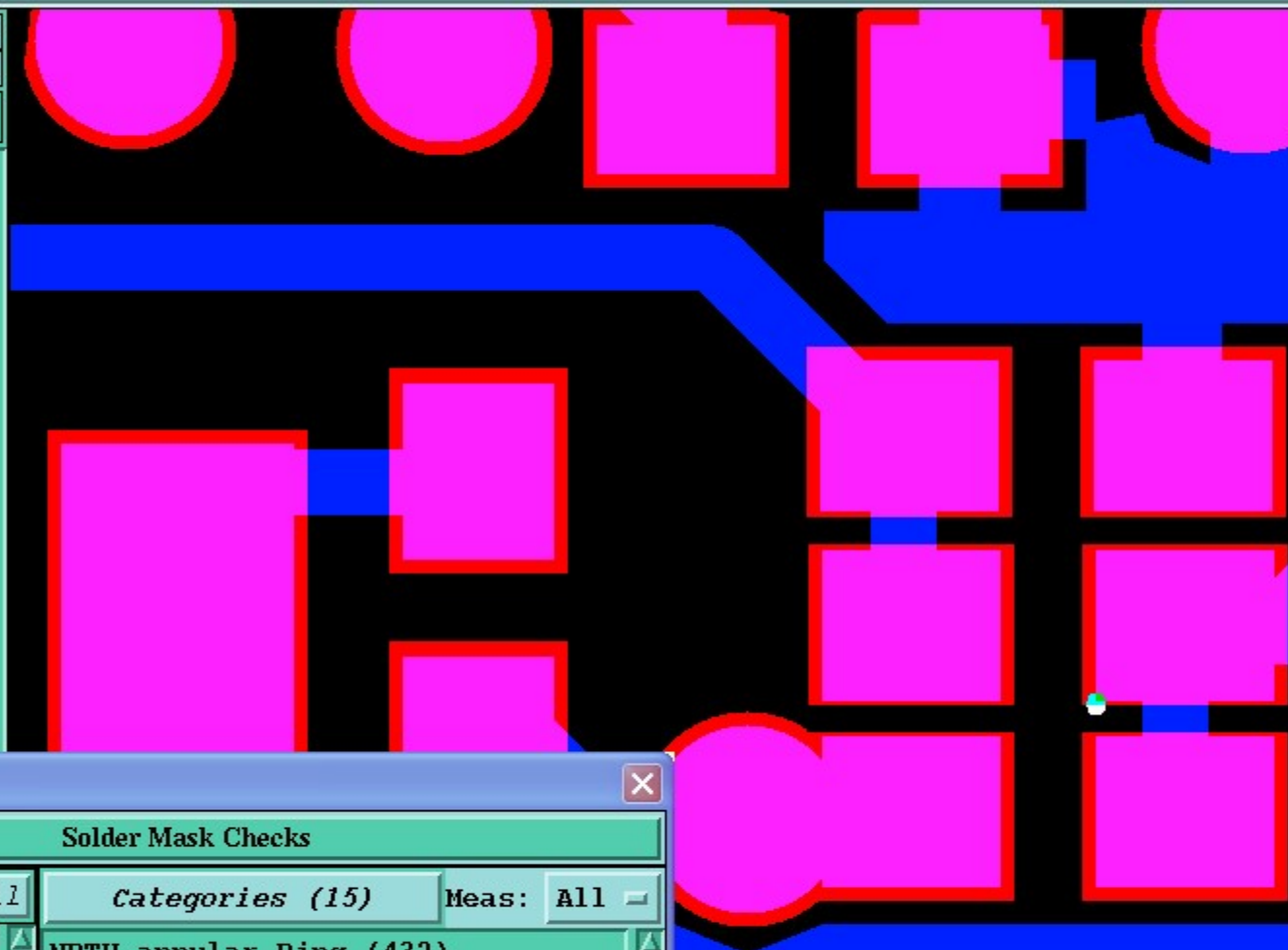
File Edit Actions Options Analysis DFM Step Rout Windows

Job : 4s88z001a0

Step: set

Job Matrix ...

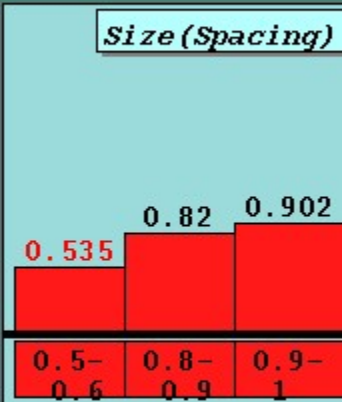
- ☐ to
- ☒ ts
- ☐ pln1
- ☐ pln2
- ☐ pln3
- ☐ sig1
- ☐ sig2
- ☐ sig3
- ☐ plnss
- ☐ ss
- ☐ bs
- ☐ bo
- ☐ dr1
- ☐ dr11-2
- ☐ dr13-4
- ☐ ldi



Results histogram

- ☒ Count
- ☒ Average
- ☒ Sum
- ☒ Min
- ☒ Max

Spacing



Spacing

DEL

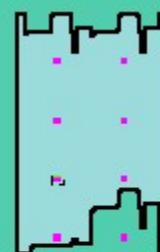
REF

Spacing

SMD

Clearance

0.1



DEL

REF

3008 measurements for 2

Close

Results viewer

Solder Mask Checks

None

Layers

All

Categories (15)

Meas: All

- ts
- bs

- NPTH annular Ring (432)
- SMD annular Ring (3008)
- Pad annular Ring (2792)
- Coverage (1168)
- Rout spacing (540)
- No bridge (144)

parameter